

Y2 – S1 CLASS TIME TABLE

(19th May 2025) to (22nd August 2025)

Semester Final Written Examinations: 2nd and 3rd September 2025

Start Time	Monday	Tuesday	Wednesday	Thursday	Friday
9.30 am	ESAE (Lec + Lab)	LEDE (Lec)	EM III (Lec)	LCSA (Lec + Lab)	Tutorial Session (LEDE & EM III)
10.30 am					
11.30 am					
BREAK					
1.00 pm		LEDE (Lab)	EM III (Lab)		MP II
2.00 pm					
3.00 pm					

Lecture Hall: 12th Floor Hall No 3

Laboratory: 7th Floor Electronic/Electrical Laboratory

Module Code and Title	Lecturers in Charge
EE060201 - Engineering Mathematics III	Mr. Lawrence Jerith
EE060202 - Embedded Systems Programming and Applications in the Environment	Mr. Dilanka Wijesena
EE060203 - Linear Electronics Design & Environment	Mr. Lawrence Benorith
EE060204 - Local Communications Systems and Applications	Mr. Charitha Maduranga
EE060205 - Mini-Project II	Mr. Lawrence Jerith